Omotoyosi Taiwo

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EDUCATION

Rensselaer Polytechnic Institute.
Electrical Engineering (M.E), 2016

Vanderbilt University.
Electrical Engineering (B.E), 2013 – GPA 3.2

Fisk University.
Computer Science (B.S.) – GPA 3.7

SKILLS

Java, Python, R, Linux, HTML / CSS
C/C++, CAD/AutoCAD, SPICE, Cadence
MATLAB, MEDICI, Android.

WORK EXPERIENCE

Freelancer
Software Engineer   Jan 2018 – Now
- Unable to work due to U.S visa complication, appeal and Canadian permanent residency application - recently approved.
- Took online data science and advance algorithm courses while working as a freelance software developer.

GLOBALFOUNDRIES
Device Engineer / Software Engineer   Sep 2016 - Dec 2017
- Designed and verified foundry ready 7nm series and parallel stacked finFETs using Cadence
- Developed web accessible automated R and python based tool for data analysis and visualization for millions of rows of experimental data to provide insights into Local Layout Effects (LLE) on device parameters - Used modern R packages like rshiny, dplyr, ggplot2, data.table, and rJava

Rensselaer Polytechnic Institute
Head Teaching Assistant   Aug 2014 - May 2016
- Head T.A for Laboratory Introduction to Embedded Control (LITEC) and Electric Circuits courses.
- Taught students and organized the rest of the TAs for lab sessions, exam proctoring, and grading coursework.

Case Western Reserve University
Intern   Jun 2011 - Aug 2011
- Developed MATLAB scripts to deblur and denoise images using matrix manipulation techniques Singular Value Decomposition (SVD), Fourier transforms and filtered deconvolution.

Fisk University Materials Science and Application Group
Student Researcher   Sep 2009 - May 2011
- Assisted with the fabrication of semiconductor crystals like CZT and SrI_2 as small as 0.5 mm thickness.
- Performed tasks that included photolithography, mechanical etching, and radiation detection measurements.

PROJECTS

Student Undergraduate Projects - Vanderbilt University.   Aug 2012 - May 2013
- Developed android application with two classmates called Footsteps that displays real-time location of friends - Designed the location and mapping algorithm using GPS signals and network towers to optimize performance.
- Redesigned with four classmates a $15x15cm^2$ PCB into a $10x10$ cm^2 PCB that detects Single Event Upsets (SEUs) on a 28nm SRAM - Wrote the embedded C code for the PIC microcontroller.